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U.S. PATENT DOCUMENTS



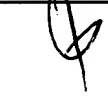

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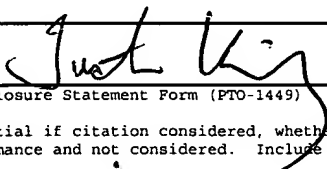
FOREIGN PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
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OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

**Examiner Initial	
	"MC68030 Enhanced 32-Bit Microprocessor User's Manual, 3rd Edition", Section 6, <u>Published by Motorola</u> , (1992)
	"PowerPC 601 RISC Microprocessor User's Manual", Chapter 4; Sections 4.7.2 through 4.7.9, <u>Available from IBM and Motorola</u>
	"TinyRISC TR4101 Building Blocks Technical Manual", Published by LSI Logic, (January 2000)
	"TinyRISC TR4101 Microprocessor Core Technical Manual", Published by LSI Logic, (December 1999)
	Tanenbaum, Andrew S., "Operating Systems: Design and Implementation", Section 2.2.3: <u>Mutual Exclusion with Busy Waiting</u> , Prentice-Hall, Inc., Englewood Cliffs, New Jersey, pp. 53-57, (1987)

Examiner 	Date Considered 5.27.02
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*Substitute Disclosure Statement Form (PTO-1449)

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